

```

State Next
0 1
1 2
2 3
3 4
4 5
5 6
6 7
7 8
8 12 (15 If /SER_WR = 0)
*9 12 (15 If /SER_WR = 0)
*10 12 (15 If /SER_WR = 0)
*11 12 (15 If /SER_WR = 0)
12 12 (15 If /SER_WR = 0)
*13 12 (15 If /SER_WR = 0)
*14 12 (15 If /SER_WR = 0)
15 15 (0 If /SER_WR = 1)

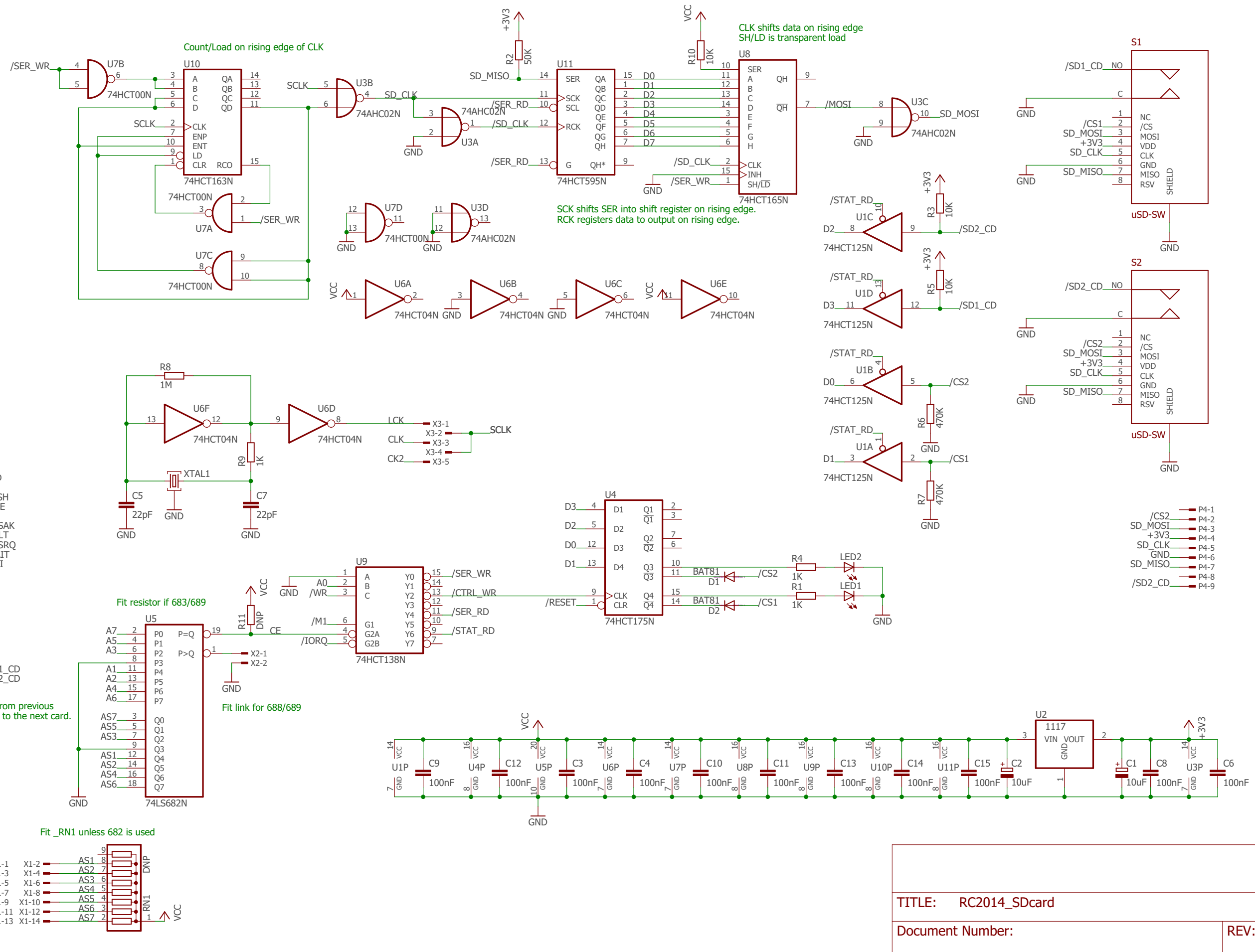
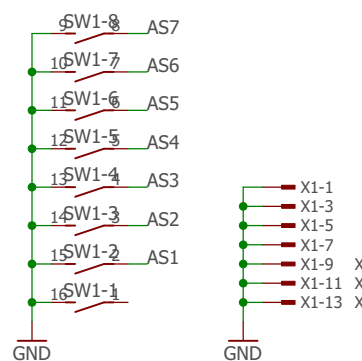
```

*not reached

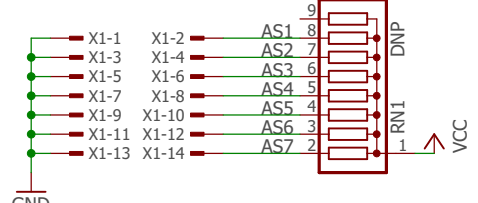
- P1-1 A15
- P1-2 A14
- P1-3 A13
- P1-4 A12
- P1-5 A11
- P1-6 A10
- P1-7 A9
- P1-8 A8
- P1-9 A7
- P1-10 A6
- P1-11 A5
- P1-12 A4
- P1-13 A3
- P1-14 A2
- P1-15 A1
- P1-16 A0
- P1-17 GND
- P1-18 VCC
- P1-19 /M1
- P1-20 /RESET
- P1-21 CLK
- P1-22 /INT
- P1-23 /MREQ
- P1-24 /WR
- P1-25 /RD
- P1-26 /IORQ
- P1-27 D0
- P1-28 D1
- P1-29 D2
- P1-30 D3
- P1-31 D4
- P1-32 D5
- P1-33 D6
- P1-34 D7
- P1-35 U1
- P1-36 U2
- P1-37 /SD1_CD
- P1-38 /SD2_CD
- P1-39 IF
- P2-1 GND
- P2-2 VCC
- P2-3 /RFSH
- P2-4 PAGE
- P2-5 CK2
- P2-6 /BUSAK
- P2-7 /HALT
- P2-8 /BUSRQ
- P2-9 /WAIT
- P2-10 /NMI

Typically U1A to U4A will be inputs from previous card and U1B to U4B will be outputs to the next card.

If not used they may be linked.



Fit _RN1 unless 682 is used



TITLE: RC2014_SDCard	
Document Number:	REV:
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